# **BASIC ELECTRONICS**

UNIT

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 $(\circ)$ 

Ans:

**Basic Electronics** 

# 16.1THERMIONIC EMISSION16.2INVESTIGATING THE PROPERTIES OF ELECTRONS16.3CATHODE-RAY OSCILLOSCOPE (C.R.O)LONG QUESTION

# 16.1 Q.1 What is meant by thermionic emission? How thermionic emission is produced? (K.B)

(GRW 2017) (Review Ex. 16.5)

### **Definition:**

### **THERMIONIC EMISSION**

"The process of emission of electrons from hot metal surfaces is called thermionic emission". **Production of Thermionic Emission:** 

Metals contain a large number of free electron. At room temperature, electrons cannot escape the metal surface due to attraction forces of atomic nucleus. If the metal is heated to a high temperature, some of the free electrons may gain sufficient energy to escape the metal surface.

### **Tungsten Filament:**

Thermionic emission can be produced by electrically heating a fine tungsten filament. Typical values of the voltage and current used for this purpose are 6V and 0.3 A respectively.

- 16.2 Q.1 How electron beam is obtained? Explain the effect of electric and magnetic field on electron beam. (*K.B+U.B+A.B*) (RWP 2017)
- **OR** Write a note on electron gun.

(DGK 2017), (Review Ex. 16.5)

- **OR** Discuses deflection of electrons by electric field.
- OR Describe, using one simple diagram in each case, what happens when a narrow beam of electrons is passed through (a) a uniform electric field (b) a uniform magnetic field. What do these results indicate about the charge on electron?

(Review Ex. 16.1)

(SGD 2016, 2017)

### Ans:

### **PRODUCTION AND PROPERTIES OF ELECTRON**

**Electron Beam:** 

Electrons are produced by the thermionic emission from a tungsten filament heated by 6V supply.

A high positive potential is applied several thousand to a cylindrical anode (+). The electrons are accelerated to a high speed and pass through the hole of the anode in the form of a fine beam of electrons. The whole setup is fitted in an evacuated glass tube.



### **Deflection of Electrons by Electric Field:**

We can setup electric field by applying a potential difference across two parallel metal plates separated by some distance. When an electron beam passes between two plates, it can be seen that the electrons are deflected toward the positive plate.



**Basic Electronics** 

### Reason:

The reason for this is that electrons are attracted by positive charges and are repelled by negative charges due to the force

 $\mathbf{F} = \mathbf{q}\mathbf{E}$ 

Where 'q' is the charge of electron and 'E' is the electric field due to plates. The degree of deflection of electrons from their original direction is proportional to the strength of the electric field applied.

### **Deflection of Electrons by Magnetic Field:**

Magnetic field is applied at right angle to the beam of electrons by using horseshoe magnet.

The spot of electron beam will be noticed on the screen due to the deflection of beam from its original (direction). Now change the direction of the horseshoe magnet. We will see the spot on the fluorescent screen is getting deflected in the opposite direction.



# 16.3 Q.1 What is cathode - rays oscilloscope (C.R.O)? Explain the working of different<br/>parts of oscilloscope? (K.B+U.B+A.B)(Review Ex. 16.2)

### Ans:

(LHR 2014, 2016, 2017, GRW 2015, 2017, SGD 2016, 2017, SHW 2017, FSD 2017, MTN 2017) <u>CATHODE – RAYS OSCILLOSCOPE</u>

The cathode – ray oscilloscope is an instrument which is used to display the magnitudes of changing electric currents or potentials.



The information is displayed on the screen of a "cathode ray tube." This screen appears a circular or rectangular window usually with a centimeter graph superimposed on it.

### **Examples**:

- Picture tube in our TV set
- Display terminal of most computers are cathode ray tubes

### **Construction**:

The cathode-ray oscilloscope (C.R.O) consists of the following components:

- The electron gun with control grid
- The deflecting plates
- A fluorescent screen

### Working of Electron Gun:

### (LHR 2015)

The electron gun consists of an electron source which is an electrically heated cathode that ejects electrons.

Electron gun also has an electrode called grid G for controlling the flow of electrons in the beam. The grid is connected to a negative potential. The more negative this potential, the more electrons will be repelled form the grid and hence fewer electrons will reach the anode and the screen. The number of the electrons reaching the screen determines. The brightness of the screen. Hence the negative potential of the grid can be used as a brightness control.

The anode is connoted to the positive potential and hence is used to accelerate the electrons. The electrons are focused into a fine beam as they pass through the anode.

### The Deflecting Plates:

After leaving electron gun, the electron beam passes between a pair of horizontal plates. A potential difference applied between these plates deflects the beam in a vertical plane. This pair of plates provides the Y-axis or vertical movement of the spot on the screen. A pair of vertical plates provides the X-axis or horizontal movement of the spot on the screen.

### The Fluorescent Screen:

The screen of cathode - ray tube consists of a thin layer of phosphor, which is a material that gives light as a result of bombardment by fast moving electrons.

### Uses of C.R.O:

The CRO is used in many fields of science, some uses are given below:

- Displaying wave forms
- Measuring voltages
- Range finding (as in radar)
- Echo sounding (to find the depth of sea beds)
- To display heart beats

### 16.1, 16.2, 16.3 SHORT QUESTIONS

### Q.1 Define electronics. (K.B)

### (SHW 2017, LHR 2015, 2017, GRW 2017)

**Ans:** Electronics is that branch of applied physics which deals with the controlled motion of electrons using different devices.

### What do you understand by thermionic emission? (K.B)

(LHR 2014, 2017, GRW 2016, 2017), BWP 2017, MTN 2017)

### Ans: Given on Page # 312

**Q.2** 

- Q.3 What happens when a narrow beam of electrons is passed through a uniform electric field? (*K.B+U.B*)
- Ans: Given on Page # 313



- Q.5 What happens when a narrow beam of electrons is passed through a uniform magnetic field? (*K.B+U.B*) (LHR 2015, GRW 2014)
- Ans: Given on Page # 313
- Q.6 When and who discovered electrons? (K.B)
- Ans:

### DISCOVERY OF ELECTRONS

In the 1850's physicists stared to examine the passage of electricity through vacuum by putting two electrodes in a sealed vacuum tube. They discovered that some kind of rays were emitted from the cathode or the negative electrode, these rays were called cathode rays. J.J Thomson in 1897 observed the deflection of cathode rays by both electric and magnetic field. From these deflection experiments, he concluded that cathode rays must carry a negative charge. These negatively charged particles were given the name of electrons.

### Q.7 How thermionic emission is produced? (*K*.*B*)

Ans:

Ans:

### PRODUCTION OF THERMIONIC EMISSION

Metals contain a large number of free elections. At room temperature electrons cannot escape the metal surface due to attraction forces of atomic nucleus. If the metal is heated to a high temperature some of the free electrons may gain sufficient energy to escape the metal surface.

- Q.8 What is cathode rays oscilloscope (C.R.O)? (K.B+A.B)
- Ans: Given on Page # 313
- Q.9 Describe functions of the electron gun. (*K*.*B*+*A*.*B*)
- Ans: Given on Page # 314
- Q.10 Write down uses of CRO. (A.B)
- Ans: Given on Page # 314

### Q.11 What are the components of CRO? (*K.B*)

- Ans: Given on Page # 314
- Q.12 How glow is produced in the tube? (*K*.*B*)

## duced in the tube? (K.B)(Do you know Pg. # 143)PRODUCTION OF GLOW IN THE TUBE

The glow in the tube is due to the circular motion of electrons in the magnetic field. The glow comes from the light emitted from the excitations of the gas atoms in the tube.

(DGK 2017)

(LHR 2014)

(SGD 2017)(Review Ex. 16.3)

(SGD 2017, DGK 2017, SHW 2017)

Ans:

**Basic Electronics** Glow in the Tube What are the functions of deflecting plates in CRO? (K.B+A.B)(LHR 2015) FUNCTIONS OF DEFLECTING PLATES The functions of deflecting plates in CRO are given below: After leaving electron gun, the electron beam passes between a pair of horizontal plates. A potential difference applied between these plates deflects the beam in a vertical plane.

- This pair of plates provides the Y-axis or vertical movement of the spot on the screen.
- A pair of vertical plates provides the X-axis or horizontal movement of the spot on the screen.

0.14 What is fluorescent screen? (*K*.*B*)

Figure:

Ans: Given on Page # 314

•

•

0.15 Why beam of electron is called cathode ray? (K.B) Ans: **CATHODE RAY** 

> The beam of electrons was called a cathode ray, because the electron had not yet been discovered. The old terminology survives in electronic engineering where a cathode-ray tube is any tube constructed along Thomson's lines whether in a computer, monitor, a television, or an oscilloscope.

0.16 How we came to know about the cathode rays? (K.B) (For your information Pg. #140) Ans: **EVIDENCE OF CATHODE RAYS** 

In a cathode-rays tube, a greenish glow is formed on the inner surface of the glass opposite the cathode, which itself is glowing orange. The shadow cast by the cross at the centre of the tube gives evidence that rays of some kind are passing through the tube.

0.17 How we can say that cathode rays move in a straight line? (*K*.*B*) (Physics Insight Pg. #140)

Ans:

### STRAIGHT LINE MOTION OF CATHODE RAYS

When an opaque object like a metal cross is placed in the path of cathode rays in a cathode ray tube, a shadow of the metal cross is formed at the end opposite to the cathode. This is an evidence that rays of some kind are passing straight through the tube.



(FSD 2017, BWP 2017)

(Do you know Pg. #143)

# Q.18In what ways an oscilloscope acts as a voltmeter? (K.B+U.B)Ans:OSCILLOSCOPE AS VOLTMETER

In order to use oscilloscope as a voltmeter, switch OFF the time base and connect the voltage to be measured to the Y-input terminals. In this way the deflection of the spot would be vertically. The deflections is proportional to potential difference, which is to be measured. In this way the input of CRO (i.e. internal resistance between Y-inputs terminals) is very height, typically several million ohms. This makes an oscilloscope very nearly an ideal voltmeter.

# When a magnet is brought near to the screen of a television tube picture on the<br/>screen is distorted. Do you know why? (K.B+U.B)(Point to ponder Pg. # 142)DISTORTION OF TV SCREEN

Electromagnets are used to deflect electrons to the desired positions of the screen of a television tube to produce clear picture. When a magnet is brought near to the screen of the television tube, the spot of the electrons beam on the screen is distorted.

Q.20 How cathode ray will deflect when it is under the inference of magnetic field. (K.B+U.B)

Ans.

).19

Ans.

### **DEFLECTION OF CATHODE RAYS**

A Cathode ray will deflect as shown when it is under the influence of an external magnetic field.



- Q.21 Is it possible for us to pass electric current through vacuum? (*Conceptual Base*)
- Ans. Current can flow through vacuum when charge carriers, electrons or ions, are transported across it. A good example are electronic vacuum tubes where electrons are emitted from a heated cathode filament and transported through vacuum by an applied electric field to a positively charged anode. The current is often controlled by applying a voltage to a grid in the path of this electron flow.
- Q.22 How the pattern of electric current is produce on the fluorescent screen of CRO? *(Conceptual Base)*
- **Ans.** The pattern of electric current is produce on the fluorescent screen of CRO when the electron produce from electron gun strike on thin layer of phosphor of fluorescent screen it produce light and draw a pattern of electric current.
- Q.23 Why the negative charge on the grid is not keep minimum for a long time? (Conceptual Base)
- **Ans.** The grid in CRO is used to control the brightness of CRO screen. If the negative charge on grid is minimum the number of electron striking on the screen is maximum and the brightness of screen is maximum. So when the brightness of screen is maximum for a long time it will destroy the screen.



	I-16 Basic Electro	nics
15.	When we heat a metal at high temperature they emit (K.B)         (A) Hole       (B) Neutron         (C) Proton       (D) Electron	901
16.	(C) Floton       (D) Election         C.R.O consists of main parts. (K.B)       (A) Five         (A) Five       (B) Four         (C) Three       (D) Two	
16.4	4 ANALOGUE AND DIGITAL ELECTRONICS	
16.5	5 BASIC OPERATIONS OF ELECTRONIC-LOGIC GAT	ES
1640	<b>LONG QUESTION</b> O 1 Write a note on analogue and digital electronics $(A B+K B)$ (LHR 2015)	2017)

Ans:

### ANALOGUE AND DIGITAL ELECTRONICS

### Analogue quantities:

Those quantities whose values vary continuously are known as analogue quantities. **Example:** 

The temperature of air varies in a continuous fashion during 24 hours of a day. If we plot a graph between time and temperature recorded at different times, we get a graph.

This graph shows that the temperature varies continuously with time. Therefore temperature is an analogue quantity. Similarly time, pressure, distance etc., are analogue quantities.

### Analogue Electronics:

The branch of electronics consisting of circuits which processes analogue quantities is called analogue electronics. **Example:** 

For example the public address system is an analogue system in which the microphone converts sound into a continuously varying electric potential. This potential is an analogue signal which is fed into an amplifier. Amplifier is an analogue circuit which amplifies the signal without changing its shape to such an extent that it Figure: Temperature Time Graph

(Review Ex. 16.6)

Other examples of analog electronics are:

- Radio
- Television
- Telephone

### **Digital Quantities:**

The quantities whose values vary in discrete steps are called digital quantities.

can operate a loudspeaker. In this way loud sound is produced by the speaker.



Digital quantities are expressed in the form of digits or numbers.

### **Digital Electronics:**

"The branch of electrons which deals with the digital quantities is called digital electronics".

For this purpose digital electronics uses only two digits 0(zero) and 1 (one) and the whole data is provided in binary system due to which processing of data becomes easy.

### <u>Examples</u>:

- For quite a long period the use of digital electronics was limited to computers only, but now-a-days its application is very wide spread.
- Modern telephone system
- Radar system
- Naval and other systems of military importance
- Devices to control the operation of industrial machines
- Medical equipments
- Household appliances

### Analogue Signal:

A continuously varying signal is called an analogue signal.

### Example:

An alternating voltage varying between the maximum value of +5V and the minimum value of -5V is an analogue signal.



and 0V. The high voltage is +5V and the low voltage is 0V.



### Analogue to Digital Converter (ADC):

"A circuit has been designed which converts the analogue signal into a digital one in the form of digits. This circuit is known as analogue to digital converter, i.e., ADC". When we get an analogue signal in the form of digits, we can process it with digital

circuit, the output of which is also in digital form.

### Digital to Analogue Converter (DAC):

"A circuit that is designed to convert digital output into analogue form by a circuit known as digital to analogue converter (DAC)".

SLampOpenOffClosedOn

As the output of DAC is an analogue signal, it can be readily sensed by us. Thus electronic systems used at present consist of both analogue and digital type circuits.

# 16.4 Q.2 What is use of ADC and DAC? Briefly explain? (K.B+A.B+U.B) Ans: ADC AND DAC

In our daily life the quantities that we perceive by our senses are usually analogue quantities which cannot be processed by digital circuits. To overcome this problem different circuits has been designed which convert analogue quantities into digital quantities and digital quantities into analogue quantities are per required. There circuits are known as ADC and DAC.

### Analogue to Digital Converter (ADC):

"A circuit has been designed which converts the analogue signal into a digital one in the form of digits. This circuit is known as analogue to digital converter, i.e., ADC".

When we get an analogue signal in the form of digits, we can process it with digital circuit, the output of which is also in digital form.

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# 16.5 Q.1 What is meant by binary (Boolean) variables? Explain with example. (K.B+U.B+A.B)

Ans:

### **BOOLEAN VARIABLES**

"The variables which have only two possible states are knows as binary variables".

### Explanation:

There are many things which have two possible sates e.g.

- A switch could be either open or closed.
- A circuit may be either ON or OFF.
- A statement would be either true or false.
- The answer of a question could be right or wrong.

All three things which have only two possible states are called binary (Boolean) variables.

### **Representation of Binary Variables:**

The state of binary variables are usually represented by the digits '0' and '1'. **Example:** 

Suppose we form a circuit by connecting a lamp to battery using a switch 'S'. We call the state of switch as input and state of current or lamp as output.

### Switch and Lamp are Binary Variables:

When the switch is open no current passes through the circuit and lamp is OFF. In other words when input is zero output is also zero.



When the switch is closed current passes through the circuit and the lamp is ON. Both switch and lamp have value '1'. Hence switch and lamp (Current) both have two possible states Zero (0) and one (1). Therefore, they are considered as binary variables. These states are called logic states and logic variables.

# 16.5 Q.2 What is meant by Boolean Algebra? Explain its importance. (K.B+A.B) Ans: BOOLEAN ALGEBRA

George Boole invented Boolean Algebra. It is also known as algebra of logics.

### **Definition**:

"It is the branch of mathematics that deals with the relationship of logic variables".

### Application:

By using Boolean algebra the values of output variables are determined when the values of input variables of circuit or system are known. Boolean Algebra handles variables that represent two types of logic propositions! 'true and false'.

### Importance:

Boolean algebra has become the foundation of digital electronic.

- It operates with two logic states '1' and '0' represented by two distinct voltage levels.
- It interpret the logical operators AND, OR and NOT.
- It develop a systematic complex digital systems.
- Simple logic gates perform the simple mathematical as well as intricate logical operations.
- Logic operations are considered as combination of switches.

### 16.4, 16.5 SHORT QUESTIONS

### Q.1 Define digital and analogue electronics. (*K.B*)

(MTN 2017, SGD 2017, RWP 2017, FSD 2017, LHR 2015, 2016, GRW 2014)

- **OR** Explain the difference between analogue and digital electronics. (Review Ex. 16.7)
- **Ans:** *Given on Page # 319*
- Q.2 Name five analogue and five digital devices that are commonly used in everyday life. (*K*.*B*+*A*.*B*) (Review Ex. 16.7)
- (K.B+A.B) (Review Ex. 16.') Ans: <u>NAMES OF ANALOGUE AND DIGITAL DEVICES</u>

The names of analogue and digital devices are as follows:

### **Analogue Devices:**

- Electric iron
- Electric fan
- Radio receiver
- Refrigerator
- Washing machine

Digital camera Mobile phone

Computer Calculator

**Digital Devices:** 

Security system

Q.3 Write the importance of digital electronics? (*K.B+A.B*) (For your information Pg. # 145) Ans: <u>IMPORTANCE OF DIGITAL ELECTRONICS</u>

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Digital technology has entered every part of our lives. Digital TV gives excellent view and allows us to be interactive. Digital cameras are faster replacing traditional film equipment. We can download an image into a PC and crop, enhance, airbrush and edit the picture. Smart ID cards are being developed. A single card can be a passport, national insurance card and driving license all in one. The card could also hold biometric data like an eye retina scan and voice scan for unique identification and security. All of this data would be held digitally in the tiny chip.

#### What is meant by logic operation? (K.B) 0.4 LOGIC OPERATION Ans:

Digital circuits perform the binary arithmetic operation with binary digits '1' and '0'. These operations are called logic function or logic operations.

#### What do you meant by logic gate? (K.B) Q.5

Ans:

### LOGIC GATES

Logic gate is a switch (digital circuit), its outputs can have only one of the two possible states. Such circuits have been designed which implement the various logic operations. These circuits are known as logic gates.

#### What are basic operations of digital electronics? (*K*.*B*) **Q.6**

Ans:

### (FSD 2017) **BASIC OPERATIONS OF DIGITAL ELECTRONICS**

Basic operations of digital electronics are:

- AND operation •
- OR operation •
- NOT operation

#### Briefly introduce Boolean algebra. (K.B) **Q.7** (Intro. to Boolean algebra Pg. # 146) **INTRODUCTION OF BOOLEAN ALGEBRA** Ans:

The algebra used to describe logic operations by symbols is called Boolean Algebra. Like ordinary algebra, English alphabets (A, B, C, etc.) and used to represent the Boolean variables. However, Boolean variable can have only two values; 0 and 1.

**Q.8** Why TV and telephone signals has been transformed from analogue to digital? (K.B)

(Do you know Pg. #146)

#### TRANSFORMATION OF ANALOGUE SIGNALS TO DIGITAL SIGNALS Ans:

TV and telephone signals once travelled as analogue signals. Electric signals in copper wire would interfere with each other and give poor quality of sound and vision. Today, everything is going digital. The big advantage of digital is quality. There is no interference or loss of strength in digital signals travelling in an optical fiber.

0.9 Show analogue signal of microphone. (K.B) (Do you know Pg. #144)

Microphone creates an analogue signal, shown by the voltage versus time graph. Ans:



### Analogue quantities are: (K.B)

(A) Whose values vary continuously (C) Temperature of air

- 2. Time, pressure, distance are all: (K.B)
  - (A) Analogue quantities (C) Nominal quantities

- (B) Whose values remain constant
- (D) All of given are true
- (B) Variable quantites
- (D) Digital quantities

3.	Which is an analogue circuit	which amplifies the signals without changing its shape
	to such an extent that it can o	perate a loudspeaker? (K.B)
	(A) Galvanometer	(B) Manometer
	(C) amplifier	(D) Optical fiber
•	The quantities whose value va	ary in non – continuous manner are called: (K.B)
	(A) Analogue quantities	(B) Digital quantities
	(C) Statistic quantities	(D) Continuous quantiies
· n	Those quantities whose value	vary continuously or remain constant: (K.B)
ЛĽ	(A) Analogue	(B) Digital
3	(C) Hybrid	(D) All of them
	Which of the following is an a	nalogue device? (K.B)
	(A) Electric fan	(B) Electric iron
	(C) Radio receiver	(D) All of them
	Electronics which provides the a	lata in the form of maximum and minimum voltage signals:
	( <b>K</b> . <b>B</b> )	0 0
	(A) Ánalogue	(B) Digital
	(C) Hybrid	(D) All of them
	Which of the following are di	gital devices? (K.B)
	(A) Computer	(B) Mobile phone
	(C) Digital camera	(D) All of them
	Circuits which convert the di	(D) rin of them gital signal into analogue signals: $(KR)$
	(A) ADC	(B) DAC
	$(\Gamma) \Gamma D C$	(D) None of them
	(C) CAD Circuits which convert the on	(D) Note of them alogue signal into digital signal: $(K R)$
•	$(\Lambda) \Lambda DC$	(B) DAC
	(A) ADC	(D) None of them
	(C) CAD Disital ale states in second terms di	(D) None of them
	Digital electronics uses two di	gits. (A.B)
	(A) 0,2	(B) 0,3
	(C) 0,1	(D) 0,4
	A switch has only possible sta	tes. $(K.B)$
	(A) Two	(B) Three
	(C) Four	(D) Five
3.	The states of binary variables	are usually represented by the digits: (K.B)
	(A) 1,2	(B) 0,2
	(C) 0,3	(D) $0,1$
<b>.</b>	George Boolean invented	a special algebra known as algebra of logics or
	(K.B)	
	(A) Boolean algebra 🦳	(B) Geometry
	(C) Ratios	(D) Trigonometry
5.	Boolean algebra operates with	two logic states represented by two distinct voltage level.
	(K.B)	
	(A) 0,2	(B) 0,3
	(C) 1.0	(D) 1.1
6.5	The number of operations of	Boolean algebra are: (K.B)
11	(A) 1	(B) 2
U	$(\mathbf{C})$ 3	$(D) \frac{1}{4}$
7	In Roolean algebra zero ropro	esents: $(KR)$
· •	( $\Delta$ ) Zero potential	(B) Ground notential
	(C) Low potential	(D) Both a $\&$ b
2	(C) LOW POTEILIAI In Declean clashing 1 manufacture	$(D) DOW a \ll 0$
ð.	III BOOIean aigebra 1 represei	$(\mathbf{A}, \mathbf{D}) $
	$(A) \supset V$	(D) Nore of these these theorem (D) Nore of the theo
	(C) Both a & b	(D) None of above

**Basic Electronics** 

#### AND OPERATION 16.6 OR OPERATION 16.7 LONG QUESTION 16.6 Q.1 What is AND operation? Explain in possible states. Write its symbol, Expression and gate? (K.B+A.B+U.B)(LHR 2015, 2017, GRW 2014, 2016, DGK 2017, MTN 2017, SHW 2017)(Review Ex. 16.10) **AND OPERATION** Ans: "AND operation is such a logic operation that its output is 1 only when all the values of its inputs are 1". **Explanation:** In order to understand the logic AND operation, we consider a circuit in which a lamp is connected to a battery using two switches $S_1$ an $S_2$ connected in series. These switches are considered as inputs and lamp is an output, this circuit is given as. **Possible States:** There are four possible states of two switches are given as When $S_1$ and $S_2$ are both open, the lamp is OFF. (i) (ii) When $S_1$ is open and $S_2$ is closed, the lamp is OFF. (iii) When $S_1$ is closed and $S_2$ is open, the lamp is ON.

(iv) When both  $S_1$  and  $S_2$  are closed, the lamp is OFF.

These states of switches and lamp are shown in table. It is clear from table that when either of the switches  $(S_1 \text{ and } S_2)$  or both are open, the lamp is OFF. When both switches are closed, the lamp is ON.

$S_1$	$S_2$	Lamp
Open	Open	OFF
Open	Closed	OFF
Closed	Open	OFF
Closed	Closed	ON



### Symbol and AND Operation:

Symbol for AND operation is dot (.)

### Expression:

Boolean expression of AND operation is

$$X = A.B$$

This expression is read as

"X equals to A AND B

### Truth Table:

"Set of inputs and outputs in binary from is called truth table".

In binary language, when either of the inputs or both the inputs are low (0), the output is slow (0). When both the inputs are high (1), the output is high (1).

These relationship are shown in table. Where 'X' represent the output. Hence AND operation may be represented by switches concede in series and each switch represents an input.

**Basic Electronics** 



### Important Results:

- When two switches are close i.e. the inputs of the AND operation are at logic '1', The output and AND operation will be at logic '1'.
- When two switches are open i.e. the inputs of AND operation are at logic '0', the output of AND operation will be at logic '0'.

### AND Gate:

"The circuit which implements the AND operation is known as AND gate".

### Symbol and AND Gate:

Symbol of AND operation is given as:



AND gate has two or more than two inputs and only one output. The value of output of AND gate is always in accordance with the truth table of AND operation. It means output of AND gate will be '1' only when all are of its input at logic '1' and for all other situations output of AND gate will be '0'.

# 16.7 Q.1 What is OR operation? Explain its possible states. Write its symbol, expression and gate? (*K*.*B*+*A*.*B*+*U*.*B*)

### (GRW 2014, 2016, 2017, FSD 2017, BWP 2017)(Review Question 16.10)

### Ans:

### OR OPERATION

"The logical operation in which the value of output variable is equal to 1 when any one of the both input variables have value equal to 1".

### **Explanation**:

In order to under the logic OR operation we consider a circuit in which a lamp is connected to a battery using two switches,  $S_1$  and  $S_2$  connected in parallel considered as two inputs.

### Possible States:

There are four possible states which are given as:

- (i) When  $S_1$  and  $S_2$  are open the lamp is OFF.
- (ii) When  $S_1$  is open and  $S_2$  closed the lamp is ON.
- (iii) When  $S_1$  is closed and  $S_2$  open the lamp is ON.
- (iv) When both  $S_1$  and  $S_2$  are closed the lamp ON.

#### $S_1$ $\mathbf{S}_2$ Lamp S, OFF Open Open Lamp Closed Open ON Closed Open ON Figure: **Circuit Diagram of OR Operation** Closed Closed ON

# MMM

All possible states of the lamp and switches are shown on the table given below.

It is clear from table that the lamp will glow if at least one of the switch i.e.  $S_1$  and  $S_2$  is closed (at logic '1')

### Symbol of OR Operation:

OR operation is represented by the symbol of plus (+).

### Expression:

Boolean expression for OR operation is given as:

$$\mathbf{X} = \mathbf{A} + \mathbf{B}$$

This expression is read as:

"X equals to A OR B"

### Truth Table:

"Set of inputs and outputs in binary form is called truth table". Truth table of OR operation is shown as:

Α	B	X=A+B
0	0	0
0	1	1
1	0	1
1	1	1

Hence OR operation may be represented by switches connected in parallel, since only one of these parallel switches need to turn on in order to flow current in the circuit.

### OR Gate:

The electronic circuits which implements the OR operation is known as OR gate".

### Symbol of OR Gate:

Symbol of 'OR' gate is given below



OR gate has two or more than two inputs and has only one output. The values of output of OR gate are always in accordance with the truth table of OR operation. It means value of output of OR gate will be '1' when anyone of its inputs is at '1' the value of output will be '0'. when both inputs are at '0'.

**Basic Electronics** 

UNIT-	-16	Basic Electronics
	16.6, 16.7 SHORT QUESTIC	NS CJ.C.
Q.1	Which of the following gates would have 1 as output. (K.	. <i>B</i> + <i>U</i> . <i>B</i> )
		)—x
N		<b>&gt;</b> ∞—x
Ans:	a and d gates would have 1 as output.	
Q.2	Civer on Proc. # 226	(LHR 2017, MTN 2017)
Ans:	Given on Fage # 520 Write down Truth table of OD gate $(K P \mid U P)$	
Q.J Ans	write down Truin table of OK gate $(A.D+U.D)$ TRUTH TABLE OF OR OPERATION	
1113.	The truth table of OR gate is given below.	
	A B $X = A + B$	
	0 0 0	
	0 1 1	
	1 0 1	
	1 1 1	
Q.4	Define AND operation. (K.B)	(RWP 2017)
Ans:	Given on Page # 325	
Q.5	Write down Truth table of AND gate. (K.B+U.B)	(LHR 2017, GRW 2016)
Ans:	TRUTH TABLE OF AND OPERATION	
	Truth table shows all the values of the input variables and t	he value of output for each set
	of the values of the inputs.	
	$\mathbf{A} \qquad \mathbf{B} \qquad \mathbf{X} = \mathbf{A} \cdot \mathbf{B}$	
		~
0(		~~1211QU
Q.0 Ans:	Define Truth table. ( $\mathbf{A}$ . $\mathbf{D}$ )	$n N N (e_{2})$
Ans.	Definition:	$\left( \left( 1\right) \right) \right) $
	"Set of inputs and outputs in binary form is called to	ruth table".
	16.6, 16.7 MULTIPLE CHOICE QU	JESTIONS
1.	<b>16.6, 16.7 MULTIPLE CHOICE QU</b> The logical operation, whose output will only be one if it	<b>JESTIONS</b> is all inputs are 1: (K.B)
1.	<b>16.6, 16.7 MULTIPLE CHOICE QU</b> The logical operation, whose output will only be one if it (A) AND (B) OR	<b>JESTIONS</b> is all inputs are 1: (K.B)
1. N	16.6, 16.7 MULTIPLE CHOICE QUThe logical operation, whose output will only be one if it(A) AND(B) OR(C) NOT(D) All of above	<b>JESTIONS</b> ts all inputs are 1: ( <i>K.B</i> ) ve
1. 2.	16.6, 16.7 MULTIPLE CHOICE QUThe logical operation, whose output will only be one if it(A) AND(B) OR(C) NOT(D) All of aboveThe logical operation, whose output will only be zero if it	<b>UESTIONS</b> is all inputs are 1: ( <i>K.B</i> ) ve its all inputs are zero: ( <i>K.B</i> )
1. 2.	16.6, 16.7 MULTIPLE CHOICE QUThe logical operation, whose output will only be one if it(A) AND(B) OR(C) NOT(D) All of aborThe logical operation, whose output will only be zero if it(A) AND(B) OR	<b>JESTIONS</b> ts all inputs are 1: ( <i>K.B</i> ) ve its all inputs are zero: ( <i>K.B</i> )
	16.6, 16.7 MULTIPLE CHOICE QUThe logical operation, whose output will only be one if it(A) AND(B) OR(C) NOT(D) All of aboveThe logical operation, whose output will only be zero if it(A) AND(B) OR(C) NOT(D) All of above(C) NOT(D) All of above	<b>UESTIONS</b> as all inputs are 1: ( <i>K.B</i> ) ve its all inputs are zero: ( <i>K.B</i> ) ve
1. 2.	<b>16.6, 16.7 MULTIPLE CHOICE QU</b> The logical operation, whose output will only be one if it         (A) AND       (B) OR         (C) NOT       (D) All of abo         The logical operation, whose output will only be zero if i         (A) AND       (B) OR         (C) NOT       (D) All of abo         The logical operation is just like       (D) All of abo	<b>UESTIONS</b> ts all inputs are 1: (K.B) ve its all inputs are zero: (K.B) ve esistors. (K.B)
1. 2. 3.	<b>16.6, 16.7 MULTIPLE CHOICE QU</b> The logical operation, whose output will only be one if it(A) AND(B) OR(C) NOT(D) All of aboveThe logical operation, whose output will only be zero if it(A) AND(B) OR(C) NOT(D) All of above(C) NOT(D) All of above(C) NOT(D) All of above(A) Series(B) Parallel(C) Not(D) All of above(A) Series(B) Parallel(C) Not(D) All of above	<b>UESTIONS</b> as all inputs are 1: (K.B) ve its all inputs are zero: (K.B) ve esistors. (K.B)

4.	AND operations is represented by:	(K.B)	UNG1000
	(A) Dot $(\bullet)$	(B) Multiplicat	ion sign
5	(C) Any sign	$(D)$ Both a $\otimes$ b	store (K B)
5.	(A) Seriel	Combinations of rest	stors. (K.B)
	(A) Serial	(B) Parallel	
( _	(C) Boin	(D) None of ad	ove
<u>p.</u>	OR operations is represented by: (1	$(\mathbf{D}) \mathbf{M} = \{1, \dots, n\}$	
NI	(A) Dot $(\bullet)$	(B) Multiplicat	ion sign
10	(C) + sign	(D) Both a & b	
7.	The various operations of Boolean	variables are also call	ed: (K.B)
	(A) Boolean constants	(B) Algebraic (	operations
•	(C) Logic operations	(D) Both b & c	
8.	The circuit which implements the A	ND operation is calle	d: (K.B)
	(A) AND gate	(B) AND circu	it
	(C) OR gate	(D) Both a & b	
9.	The circuit which implements the C	<b>)R</b> operation is called	: (K.B)
	(A) AND gate	(B) OR circuit	
	(C) OR gate	(D) Both b & c	
10.	In case of OR operation the lamp is	Off when: (K.B)	
	(A) $S_1$ and $S_2$ are open	(B) $S_1$ is open a	and $S_2$ is closed
	(C) $S_1$ is closed and $S_2$ is open	(D) $S_1$ and $S_2$ a	re closed
11.	OR operation is represented by the	symbol of (+) and Bo	oolean expression for OR is:
	( <b>K</b> . <b>B</b> )		
	(A) x = A + B	$(\mathbf{B}) \mathbf{x} = \mathbf{A} - \mathbf{B}$	
	(C) $x + A = A$	(D) $X = \overline{A+B}$	
12.	The output of OR operation is 0 wh	en: (K.B)	
	(A) $A = 0, B = 0$	(B) $A = 1, B =$	1
	(C) $A = 0, B = 1$	(D) $A = 1, B =$	0
16.8	B NOT	OPERATION	
16.9		ND GATE	- ran
	LON	g questions	21/16.100
16.8 (	Q.1 What is meant by NOT operation	? Explain its possible	states, write its symbol,
	Expression and gate. (K.B+U.B+A	$(\mathbf{L}, \mathbf{B}) \cap (\mathbf{L}, \mathbf{C})$	HR 2015)(Review Question 16.10)
Ans:		DT OPERATION	
	"A logical operation which changes the	he state of binary (Boo	lean) variable".
	OR	Γ	
-	"Not operation inverts the value of Bo	oolean variable".	Lamp
N	Explanation:		( <sup>(00)</sup>
N	In order to understand NOT operat	ion, we consider a	U
) 0	circuit in which a lamp is connected	to a battery with a	S ,
	switch 'S in parallel way.		
	Possible States:		
	NOT operation has only one input and	d only one output	+ <b>I</b>
	There are two possible sates		Figure: <u>Circuit Diagram of</u>
	There are this possible bares.		NOT Gate
		F	

- When the switch "S' is open, the current will pass through the lamp and it will glow.
- When the switch is closed, no current will pass through the lamp due to large resistance of its filament and it will not glow.
   S LAMP OFF ON ON OFF

### Symbol of NOT operation:

NOT operation is represented by a line or bar over the symbol i.e. X = A. **Expression:** 

Boolean expression for NOT operation is given as:

$$X = \overline{A}$$
  
"X equals A NOT".

This is read as: **Truth Table:** 

"A set of inputs and outputs in binary form is called truth table". If the value of Boolean variable is 1, then after NOT operation it values would change to 0. Similarly, if it values before NOT operation, then after NOT operation it would change to '1'.

Α	$\mathbf{X} = \overline{\mathbf{A}}$
0	1
1	0

"Thus NOT operation inverts the state of Boolean variable".

### NOT Gate:

The electronic circuit which implements NOT operation is known as NOT gate.

### Symbol of NOT Gate:

The symbol of NOT gate is given as NOT gate has only one input and one output terminal NOT gate works in such a way that if its input is '0' is output would be '1'. It its input is '1' the nits output would be '0'. Not gate performs the basic logical function called inversion of complementation. Not gate is also called inverter.



### **<u>Purpose of Gate</u>**:

Ans:

The purpose of this gate is to convert one logic to opposite logic level into the opposite logic level. When a high level is applied to an inverter, a low level appears on its output in vice versa.

16.9 Q.1 What is NAND gate? Explain its symbol. Express and Truth table? (K.B+U.B+A.B)

(LHR 2017, GRW 2014)

### NAND GATE

NAND operation is simply AND operation followed by a NOT operation. "the NAND gate is obtained by coupling a NOT gate with the output terminal of the AND gate".



### Symbol of NAND Gate:

Symbol of NAND gate is given as: The NOT gate inverts the output of the AND gate.

### Short Symbol of NAND Gate:

In this symbol the NOT gate has been replaced with a small circle. this small Circle attached to a the output of NAND gate shows NOT operation.



### Expression:

Boolean expression for NAND operation is described as: The output of the NAND gate equals A.B and is written as:

 $X = \overline{A.B}$ 

It is read as

"X equals A AND B NOT".

### Truth Table:

В	Α	$\mathbf{X} = \overline{\mathbf{A}.\mathbf{B}}$
0	0	1
0	1	1
1	0	1
1	1	0

Hence it is clear form table that inverts the output of the NAND gate.

### 16.8, 16.9 SHORT QUESTIONS

**NOT GATE** 

Q.1 Which gate perform logic complementations? (*K.B*) OR Define NOT Gate.

(SHW 2017) (RWP 2017)

(BWP 2017)

(BWP 2017)

### **Definition:**

Ans:

Ans:

"An operation after which the Boolean variable changes its state and acquires the second possible state is known as NOT operation". NOT gate performs logic complementation.

- Q.2 Write down Truth table of NOT gate. (U.B+K.B)
- Ans: Given on Page # 330
- Q.3 Define NAND Gate. (K.B)
- Ans: Given on Page # 330
- Q.4 Write down Truth table of NAND gate. (U.B+K.B)
- Ans: *Given on Page # 331* Q.5 Write down the purp
  - Write down the purpose of NOT Gate? (K.B+A.B)

### PURPOSE OF NOT GATE

The purpose of NOT gate is as follows:

The purpose of this gate is to convert one logic to opposite logic level into the opposite logic level. And a high level is applied to an inverter, a low level appears on its output in vice versa.

		16	Basic	Electronics
		16.8. 16.9 MULTIPLE C	HOICE QUESTIONS	01000
	1.	NOT operation is represented by: ( <i>K.B</i> )	Basic Electronics         ULTIPLE CHOICE OUESTIONS         (B) bar over the symbol         (D) (.) dot         (D) (.) dot         bar over the symbol         (B) +1         (D) -2         evalue of Boolean variable 0 is: (K.B+U.B)         (B) +1         (D) 1         (KB)         (B) inverter         (D) subs tractor         It an AND operation followed by a: (K.B)         (B) OR operation         (D) AND operation         (D) AND operation         (D) All of above         OT operation are: (K.B)         (B) 2         (D) 4         to implement NOT operation: (K.B)         (B) NOT gate         (D) Both a & b         hoth a & b         to implement NOT operation: (K.B)         (B) A=1, B=0         (D) None of	
		(A) line	(B) bar over the symbol	
		(C) both A & B	(D) (.) dot	
	2.	Value of a Boolean variable 1 after NOT	operation is: (K.B+U.B)	
0	AN	(A) 0	(B) +1	
NAD	N)	(C) -1	(D) -2	
90,	3.	After NOT operation the value of Boolea	n variable 0 is: (K.B+U.B)	
		(A) 0	$(\mathbf{B}) + 1$	
		(C) -1	(D) 1	
	4.	NOT gate is also called: (K.B)		
		(A) converter	(B) inverter	
		(C) adder	(D) subs tractor	
	5.	NAND operation is simply an AND operation	ation followed by a: (K.B)	
		(A) NOR operation	(B) OR operation	
		(C) NOT operation	(D) AND operation	
	6.	NOT operation is also known as: (K.B)		
		(A) Gate	(B) Inverter	
		(C) Converse	(D) All of above	
	7.	Number of input(s) of NOT operation ar	e: ( <i>K</i> . <i>B</i> )	
		(A) 1	(B) 2	
		(C) 3	(D) 4	
	8.	The circuit which is used to implement N	OT operation: (K.B)	
		(A) AND gate	(B) NOT gate	
		(C) OR gate	(D) Both a & b	
	9.	NAND gate is the combination of: ( <i>K</i> . <i>B</i> )		- 60
		(A) AND & OR	(B) AND & NOT	
		(C) NOT & OR	(D) None of them	21 GOV
	10.	A and B are two inputs of NAND gate. It	s output would be zero when:	(K.B)
		(A) A=0, B=0	(B) A=1, B=0	
		(C) A=0, B=1	(D) A=1, B=1	
	11.	The equation of NOT operation is	.(K.B)	(GRW 2016)
		(A) X = A. B	(B) X = A + B	
	2 m	(C) X = A - B	(D) $X = \overline{A}$	
NA	12.	The output of NAND gate is 0 when	. (K.B)	(RWP 2017)
MA /	5-0	(A) A=0, B=0	(B) $A=1, B=1$	()
~		(C) A=0, B=1	(D) A=1, B=0	

 $\sim$ 



### House Safety Alarm:

We can use single NAND gate to make burglar alarm. This can be done by using NAND gate, an LDR, a push – button switch S and an alarm. Connect LDR between NAND gate input B and the positive terminal of the battery. the LDR will cause a HIGH level input '1' at B when in light because of its low resistance. The LDR will cause a LOW level input '0' at B when light is interrupted and causes high resistance in LDR. A LOW level signal is also caused at A when burglar steps on switch S. So this burglar alarm sounds when either burglar interrupts light falling on LDR or steps on switch S.



### 16.10, 16.11 SHORT QUESTIONS

- Q.1 Define NOR  $\overline{\text{Gate.}(K.B)}$
- Ans: Given on Page # 333
- Q.2 Write down Truth table of NOR gate. (*K*.*B*+*U*.*B*)
- Ans: \_Given on Page # 333
- Q.3 What is bit and byte? (*K*.*B*)
  - <u>BIT AND BYTE</u>

A bit represents data using 1' and 0's. Eight bits is equal to 1 byte.

### Q.4 What is digitization? (*K*.*B*)

Ans:

Ans:

### **DIGITIZATION**

Digitization is the process of transforming information into 1's and 0's.
 Q.5 Assume you have an OR gate with two inputs, A and B. Determine the output C, for the following cases: (U.B) (Quick Quiz Pg. # 151)

(a) 
$$A = 1, B = 0$$
 (b)  $A = 0, B = 1$ 

### If either input is one, what is the output?

**Ans.** The value of the output of OR gate will '1' when either of its inputs is '1'. Thus, in this case, the output C will be '1'.

$$X = \overline{\overline{A}} = A$$

$$X = \overline{A + B} = A + B$$

$$X = \overline{\overline{A.B}} = A.B$$

Here double line indicates double NOT operation.

(LHR 2014, 2015)

(For your information Pg. #151)

(For your information Pg. #151)

UNIT-	16	Basic Electronics				
Q.6	How we can obtain NOT gate from NA	AND or NOR gate explain with the help of				
Ans:	truth table? (K.B+U.B)	(For your information Pg. # 150) <u>OF NOT GATE</u>				
M	Formation of NOT gate from NAND and NOT A Output 0 1 1 0 A Figure: Formation of NOT gate from resultant truth tables	OR gates with the resultant truth table. A Output 0 1 1 0 A Output NAND and NOR gates with the				
1.	<b>16.10, 16.11 MULTIPLE</b> The NOR operation is simply an OR oper	CHOICE QUESTIONS ration followed by a: (K.B)				
	(A) NOT operation	(B) AND operation				
	(C) NAND operation	(D) OR operation				
2.	The Boolean expression for NOR operation	on is: ( <i>K</i> . <i>B</i> )				
	(A) $X = \overline{A+B}$	(B) X = A - B				
	(C) $X = A + B$	(D) $X = \overline{A.B}$				
3.	To make burglar alarm, we use: (A.B)					
	(A) NAND gate	(B) OR gate				
	(C) NOT gate	(D) NOR gate				
4.	NOR gate is the combination of: (K.B)					
	(A) AND & OR	(B) AND & NOT				
	(C) NOT & OR	(D) None of them				
5.	A and B are the two input of NOR gate. I	ts output would be 1 when: (U.B)				
	(A) A=0, B=0	(B) A=1, B=0				
M	(C) $A=0, B=1$	(D) A=1, B=1				

<u>UNIT-16</u>

Basic Electronics

			M	CQ'S	ANS	NER	KEY	TOPI	C WI	SE)			
			1	6.1		HERN	IIONI	C EM	ISSI	ON			_
	16.2		NES	TIGA	TING	THE	PRO	PERT	IES	OF EL	.ECTF	ONS	
		16.3		САТ	HODE	ERAY	050	ILLO	SCO	PE (C	.R.O)		
-	1_	2	3	4	5	6	7	8	9	10	11	12	
NN	13	14	C 15	C	A	B	A	B	D	D	C	B	
90	A	B	D	C									
		16.4		ANAI	LOGU	E AN	D DIO	SITAL		CTRO	DNICS		
	16.5	B	ASIC	C OPE	RATI	IONS	OF E	LECT	RON	IC-LO	GIC	BATES	
	1	2	3	4	5	6	7	8	9	10	11	12	
	D 13	A 14	C 15	B 16	A	D 18	В	D	B	A	C	A	
	D	A	C	B	D	A							
				16	.6	AN	D OP	ERAT	ON				
				16	6.7	OF	R OPE	RATI	ON				
	1	2	3	4	5	6	7	8	9	10	11	12	
	A	В	А	A	В	C	C	A	A	A	A	А	
				16	.8	NO	τ ορι	ERAT	ON				
					16.9		NAND	GAT	Ξ				_
	1 D	2	3 D	4 D	5	6 D	7 D	8 D	9 D	10	111 D	12 D	
	D	А	D	D	46.4							D	~~~
								GATE			$\bigcirc$	R	อโป
			Ĺ	6.11	<u> </u>	SES			GAT	ê	61	°CC	900
					A	A		4 C	5 A	$\langle \Sigma \rangle$	C.		
			$\square$	~	-1V	7110	$\left( 0\right)$	(U)	0	- D			
		g	115	$\tilde{\alpha}$	1//	70							
		\ n \ `		JU	00								
200	MN	Nor	المار										
11/1/	JU V												



- 16.2 Explain the working of different parts of oscilloscope. (K.B+U.B+A.B)
- **Ans:** (See Topic 16.1, 16.2 & 16.3, Long Question-3)

UNIT	-16 Basic Electronics		
16.3	Name some uses of oscilloscope. (A.B)		
Ans:	USES OF CRO		
	The CRO is used in many fields of science, some uses are given below:		
	• Displaying wave forms		
	• Measuring voltages		
	• Range finding (as in radar)		
N	• Echo – sounding (to find the depth of sea – beds)		
U	• To display heart beats		
6.4	Considering an oscilloscope explain.		
i)	How the filament is heated? $(K.B)$		
as:	Filament is heated electrically by a battery (6V Supply).		
i)	Why the filament is heated? ( <i>K</i> . <i>B</i> )		
ns:	By heating filament a fine beam of electrons is obtained.		
ii)	Why the anode potential is positive with respect to the cathode potential. (K.B)		
ns:	To accelerate the electrons emitted form heated filament positive potential of anode is used.		
	In this way the electrons are focused into a fine beam as they pass through the anode.		
<b>v</b> )	Why a large potential is applied between anode and cathode. $(K.B+U.B)$		
ns:	After leaving the electron gun, electron beam passes between pair of horizontal. A large		
	potential difference is applied between anode and cathode, due to this potential electrons		
	are directed in specific direction. Higher voltage in short time produced and excellent displaying ways forms beight voltage supply also best the filement swightly and increased		
	the rate of thermo ionic emission		
v)	Why the tube evacuated? $(K B)$		
ns:	Ionization of gases present in tube occur due to height voltage applied across tube so it		
	must be evacuated. Due to ionization of gases a fine beam of electrons cold not be		
	produced and accelerate in specific direction.		
5.5	What is electron gun? Describe the process of the thermionic emission		
	(K.B+A.B+U.B)		
ns.	(See Topic 16.1,16.2 & 16.3 Long Question-1 & 2)		
6.6	What do you understand by digital and analog quantities? (K.B)		
ns.	(See Topic 16.4, Long Question-1)		
6.7	Differentiate between analog electronics and digital electrons. Write down names of		
	five analogue and five digital devices that are commonly used in every day.		
	(K,B+A,B)		
ns:	The differences between enclosus and digital electronics are given below:		
	The differences between analogue and digital electronics are given below.		
N	Analogue Electronics         Digital Electronics		
1017	• The branch of electronics consisting • The branch of electronics which deal		
$\cup$	• The branch of electronics consisting of such circuits which process the with the digital quantities is called		
	analogue quantities (continuously digital electronics		
	vary) is called analogue electronics.		
	PHISICS-10 338		



### CONCEPTUAL QUESTIONS

FACTORS ENHANCING THERMIONIC EMISSION

### 16.1 Name two factors which can enhance thermionic emission.

Ans:

The factors which enhance thermionic emission are as follows:

- Rate of thermionic emission depends upon the nature of the metal used, temperature and surface area of the metal.
- By increasing the temperature and surface area of the cathode, rate of thermionic emission can be increased.

# 16.2 Give three reasons to support the evidence that cathode rays are negatively charged electrons. Ans: NEGATIVE CHARGE ON ELECTRONS

IN the beginning, no one was sure about the nature of cathode-rays. It was J.J. Thomson who carried out many experiments and concluded that cathode-rays are negatively charged electrons. The three reasons to support this evidence are as follows.

- They are attracted towards positively charged plate.
- They are deflected in magnetic field opposite to the direction of positive charge.
- Their charge to mass ratio (e/m) is equal to e/m of electrons.

16.3 When electrons pass through two parallel plates having opposite charges they are deflected towards the positively charged plate. What important characteristics of the electron can be inferred form this?

### Ans:

### PROPERTY OF ELECTRON

From the deflection of electrons towards the positively charged plate, we can easily conclude that electrons carry negative charge.

16.4 When a moving electron enters the magnetic field it is deflected from its straight path. Name two factors which can enhance electron deflection.

Ans:

16.6 Ans:

### FACTORS ENHANCING ELECTRON DEFLECTION

The factors which enhance electron deflection are as follows:

- Strength of magnetic field
- Speed of electron.

### 16.5 How can you compare the logic operation X = A.B with usual operation of multiplication? Ans: <u>COMPARISON</u>

From the truth table of AND operation it is clear that behave as multiplicative inverse. Each time result is zero when is multiplied with any Boolean variable. Hence logic operation X = A.B behave as operation of multiplication.

### NAND gate is the reciprocal of AND gate. Discuss. <u>REFCIPROCAL OF AND GATE</u>

In NAND gate the value of AND gate is inverted by NOT gate. From the sets of inputs and output given in truth table of NAND gate, its is clear that it is the reciprocal of AND gate i.e. every time the value of output of AND gate is

#### 16.7 Show that the circuit given as below acts as OR gate.

AO

### Ans:

### **OR GATE**

The electronic circuit which implements the OR operation is known as OR gate. It has two or more than two inputs and has only one output. The values of output of OR gate are always in accordance with the truth table of OR operation. It means, the value of output of OR gate will be 'l' when one of its inputs is at 'l'. The output will be '0', when both inputs are at '0'.

Α	В	$\mathbf{X} = \mathbf{A} + \mathbf{B}$	$X = \overline{A + B}$	$X = \overline{\overline{A + B}}$
0	0	0	1	0
0	1	1	0	1
1	0	1	0	1
1	1	1	0	1

16.8 Show that the circuit given as below acts as AND gate.



### Ans:

### AND GATE

The circuit which implements the AND operation is known as AND gate. AND gate has two or more than two inputs and only one output. The value of output of NAND gate is always in accordance with the truth table of AND operation. It means output of AND gate will be 'l' only when both of its inputs are at logic 'l', for all other situations output of AND gate will be '0'.

Α	В	$\overline{\mathbf{A}}$	B	$\overline{\mathbf{A}}.\overline{\mathbf{B}}$	$\overline{\overline{\mathbf{A}}.\overline{\mathbf{B}}}$	~~~~
0	0	1	1	1	0	STAN
0	1	1	0		2)0	0)[[[[[[
1	0	0	7 170		5100	
1	1	0	0	$\left( \right) \left( \left( \right) \left( \right) \left( \right) \left( \left( \right) \left( \right) \left( $	2	
What is LDR	? [	10010	1    U	עענ		-

#### 16.9 What is LDR?

The light-dependent resistor or photoresistor is a passive component that decreases Ans. resistance with respect to receiving luminosity (light) on the component's sensitive surface. The resistance of a photoresistor decreases with increase in incident intensity; in other words, it exhibits photoconductivity. A photoresistor can be applied in light sensitive detector circuits and light activated and dark-activated switching circuits acting as a resistance semiconductor.

	<u>UNIT-</u>	-16	Basic Elec	tronics			
	Time: Q.1	SELF T 40 min. Four possible answers (A), (B), (C) & (	EST (D) to each question are given, r	farks: 25 nark the			
		correct answer.		( <b>6</b> ×1=6)			
l I	1.	NAND operation is simply an AND opera	tion followed by a:				
N	NN	(A) NOR operation	(B) OR operation				
NV)	90	(C) NOT operation	(D) AND operation				
	2.	A and B are two inputs of NAND gate. Its	output would be zero when:				
l		(A) $A = 0, B = 0$	(B) $A = 1, B = 0$				
l		(C) $A = 0, B = 1$	(D) $A = 1, B = 1$				
l	3.	The various operations of Boolean variables are also called:					
		(A) Boolean constants	(B) Algebraic operations				
l		(C) Logic operations	(D) Both b & c				
l	4.	Digital electronics uses two digits.					
l		(A) '0' and '2'	(B) '0' and '3'				
		(C) '0' and '1'	(D) '0' and '4'				
	5.	The main components of C.R.O are:					
		(A) Five	(B) Four				
l		(C) Three	(D) Two				
l	6.	Typical value of the voltage and current u	sed for thermionic emission are:				
l		(A) 3v and 0.4A	(B) 6V and 0.3A				
l		(C) 5V and 0.3A	(D) 6V and 0.1A				
l	Q.2	Give short answers to following questions		(5×2=10)			
l		i. Define thermionic emission.	20	COMM			
l		ii. Differentiate between digital and analogue quantities.					
l	0						
		iv. Define AND operation. Also draw its cir	cuit diagram.				
I		v. What are the uses of logic gates?					
l	Q.3	Answer the following questions in detail.		(4+5=9)			
1	-	a) Write a note on C.R.O.					
n	NN	b) What is OR operation? Draw its circuit diagram and truth table.					
NN Y	Note:	ote:					
Parents or guardians can conduct this test in their supervision in order to check the sk							
l	of students.						
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