Q.2	Cho	ose the correct answer.						
1.	Data and programs that are not being use by computer are stored in:							
	(a)	Secondary storage	(b)	Cache				
	(c)	Primary storage	(d)	printer				
2.	A set	A set of instructions that run the computer are:						
	(a)	Hardware	(b)	document				
	(c)	CPUs	(d)	Software				
3.	The i	The idea of stored program was given by:						
	(a)	Charles Babbage	(b)	John Leitbiz				
	(c)	John Von Neumann	(d)	Pascal				
4.	Whic	ch one is the brain of compute	er?					
	(a)	RAM	(b)	CPU				
	(c)	Motherboard	(d)	System unit				
5.	Which component of a computer decodes and executes program constructions?							
	(a)	Main memory	(b)	Control unit				
	(c)	Bus interconnection	(d)	ALU				
6.	The component of computer that stores data and program while these are being executed is called:							
	(a)	Main memory	(b)	Control unit				
	(c)	Bus interconnection	(d)	ALU				
7.	The component of computer that is used to connect different parts of the computer together is called:							
	(a)	Register	(b)	I/O unit				
	(c)	Bus interconnection	(d)	CPU				
8.	A computer drives its basic strength from:							
	(a)	Speed	(b)	Memory				
	(c)	Accuracy	(d)	All				
9.	The program that contains instructions to operate a device is called:							
	(a)	Device driver	(b)	Device Operator				
	(c)	Device linking	(d)	Device system				
10.	Which component of CPU is responsible for performing arithmetic and logical operations on numeric data?							
	(a)	ALU	(b)	Control unit				
	(c)	Register	(d)	None				

Ü	CP	U is an example of:		
	(a)	Software	(b)	Input unit
	(c)	Hardware	(d)	Output unit
	Wh	ich is a storage device?		
	(a)	CPU	(b)	Clock
	(c)	Floppy disk	(d)	Bus
·	Wh	ich component of computer coor	dinat	es all activities in the computer?
	(a)	ALU	(b)	Control unit
	(c)	Bus interconnection	(d)	Registers
9	Wh	ich component of computer is resp	oonsil	ole for comparison of two numbers?
	(a)	ALU	(b)	Control unit
	(c)	Memory	(d)	Hard Disk
	-	—— is the component of CPU		
	(a)	Main memory	(b)	Bus interconnection
	(c)	ALU	(d)	I/O unit
	CP	U includes all of the following co	ompo	nents except?
	(a)	Main memory	(b)	ALU
	(c)	Control unit	(d)	Registers
	The	cells of memory are logically or	rganiz	zed into group of:
	(a)	4-bits	(b)	5-bits
	(c)	7-bits	(d)	8-bits
	CP	U is an example of:		
	(a)	Software	(b)	a program
	(c)	Hardware	(d)	an output unit
	The	address of instruction under the	proc	essor execution is contained within
	(a)	Program Counter	(b)	Current Instruction Register
	(c)	Memory address registers	(d)	Memory Buffer register
	RA	M is a:		
	(a)	Read Only Memory	(b)	Permanent memory
	(c)	Volatile memory	(d)	None
		is non-volatile memory:		
	(a)	RAM	(b)	DRAM
	(c)	ROM	(d)	SRAM

	—— chips must be refreshed	d with elect	ric charge periodically?
(a)	SRAM	(b)	ROM
(c)	EEPROM	(d)	DRAM
RO	M is:		
(a)	Read Only Memory	(b)	Non-volatile
(c)	Volatile	(d)	Both a and b
	——is a type of ROM?		
(a)	PROM	(b)	EPROM
(c)	EEPROM	(d)	All
	—— memories can be re-wr	itten by usi	ng electrical devices?
(a)	PROM	(b)	ROM
(c)	EEPROM	(d)	EPROM
Wh	ich memory is used to increa	se the proc	essing speed of computer?
(a)	RAM	(b)	ROM
(c)	Cache Memory	(d)	SRAM
File	es not being used by compute	r are stored	d in:
(a)	Secondary storage	(b)	Cache
(c)	Primary Storage	(d)	Registers
A s	et of parallel electrical lines u	ised to tran	nsfer data is called:
(a)	Monitor	(b)	Computer Clock
(c)	I/O unit	(d)	Bus
The	e circuit board on which proce	essor and c	other chips are mounted is called:
(a)	Master board	(b)	Main board
(c)	Motherboard	(d)	All
The	e external devices that are con	nected to	a computer are called:
(a)	Buses	(b)	Registers
(c)	Peripherals	(d)	Slots
A c	omputer drives its basic stren	igth from:	
(a)	Speed	(b) N	Memory
(c)	Accuracy	(d) A	ALL of the above
The	e arithmetic and logic unit per	rforms the	following operations:
(a)	Controls Computer operation	ons	
(b)			addition and subtraction etc.
(c)	Performs logical comparison	ns such as	equal, greater than, less than etc.
(d)	Both b and c		

33.	How many categories the system bus is divided?						
	(a)	2	(b)	3			
	(c)	4	(d)	5			
34.	Which one is not a type of bus?						
	(a)	Cache	(b)	Control bus			
	(c)	Address bus	(d)	Data bus			
35.	CPU	CPU sends command signals to different components of computer through:					
	(a)	Data bus	(b)	Address bus			
	(c)	Control bus	(d)	All			
36.	A bu	A bus that communicates data between CPU and memory is called:					
	(a)	Data bus	(b)	Address bus			
	(c)	Control bus	(d)	All			
37.		——command is used to w	rite data ii	nto memory?			
	(a)	I/O Write	(b)	Transfer Ask			
	(c)	Memory Write	(d)	I/O Read			
38.	Temporary storage area within CPU is called:						
	(a)	Register	(b)	ALU			
	(c)	Bus interconnection	(d)	Linked code			
39.	Which one is faster?						
	(a)	RAM	(b)	Cache			
	(c)	Register	(d)	Hard disk			
40.	Whic	ch is not a type of register?					
	(a)	Math coprocessor	(b)	Flag			
	(c)	Accumulator	(d)	Segment			
41.	exect	registers holds the acution:	ldress of	the next instruction to be fetched for			
	(a)	IR	(b)	PC			
	(c)	MAR	(d)	MBR			

	——registers holds	s the instruction fer	ched from memory?	
(a)	IR	(b)	PC	
(c)	MAR	(d)	MBR	
	ch register holds the ssed by the CPU?	e address of active	memory location the	nat is being currently
(a)	IR	(b)	PC	
(c)	MAR	(d)	MBR	
Whic	ch is a storage devi	ce?		
(a)	CPU	(b)	Clock	
(c)	Floppy disk	(d)	Bus	
Whic	ch component is res	sponsible for com	paring the contents o	f two pieces of data:
(a)	ALU	(b)	Control Unit	
(c)	Memory	(d)	None	
Whice mem	_	he contents of d	ata/instruction read	from, or written in
(a)	IR	(b)	PC	
(c)	MAR	(d)	MBR	
	—— registers acts	as counter for loc	ping process?	
(a)	EAX	(b)	EBX	
(c)	ECX	(d)	EDX	
	—— registers is us	sed for arithmetic	and data operations?	
(a)	AX	(b)	BX	
(c)	CX	(d)	DX	
	—— registers hol	ds the base locat	on of all executable	e instructions in the
progr	476-27			
(a)	CS	(b)	DS, SS	
(c)	ES, FS	(d)	FS, GS	
	—— registers cont	ains the base loca	tion of the current pr	rogram stack?
(a)	CS	(b)	DS	
(c)	ES	(d)	SS	
The	size of segment reg	ister is:		
(a)	2-bytes	(b)	2 KB	

	(c)	2 MB	(d)	2 bits
52.	The 1	program that contains instruction	ons to	operate a device is called:
	(a)	Device driver	(b)	Device operator
	(c)	Device linking	(d)	Device system
53.	Whic	ch component of CPU decodes	and e	executes the programs instructions?
	(a)	Clock	(b)	CU
	(c)	Cache	(d)	ALU
54.	Whice device		ocess	or's communication with its peripheral
	(a)	I/O Unit	(b)	Hard Disk
	(c)	Registers	(d)	CD
55.	Whic	ch is not a component of a CPU	J?	
	(a)	Clock	(b)	Register
	(c)	Main Memory	(d)	Cache
56.	The 1	basic unit for data storage is:		
	(a)	Byte	(b)	Bit
	(c)	ТВ	(d)	GB
57.	8 By	tes is equal to:		
	(a)	Bit	(b)	48 bits
	(c)	32 Bits	(d)	64 bits
58.	The 1	process of taking action on the	decod	led instruction is called:
	(a)	Decode instruction	(b)	Execute instruction
	(c)	Activate instruction	(d)	None
59.	The calle	-	ction	so that the computer can understand it is
	(a)	Decode instruction	(b)	Execute instruction
	(c)	Translate instruction	(d)	Fetch instruction
60.	The 1	programming languages that ar	e clos	se to human language are called:
	(a)	High-level	(b)	Low-level

	(c)	Medium-level	(d)	Machine
61.	The p	orogramming languages that a	re very	close to machine code are called:
	(a)	High-level	(b)	Low-level
	(c)	Medium-level	(d)	Machine
62.	How	many types of language transl	ator?	
	(a)	2	(b)	4
	(c)	5	(d)	3
63.		——is a type of language tran	ıslator	?
	(a)	Compiler	(b)	Assembler
	(c)	Interpreter	(d)	All
64.	The s	et of parallel lines to connect	to diff	erent parts of the computer is called:
	(a)]	Bus	(b)	Memory
	(c) I	7/O	d)	ALL of the above
65.	Whic	h register is used for counting	purpo	se?
	(a) A	AX	(b)	CX
	(c) I	OX	(d)	BX
66.	Whic	h register is known as Accum	ulator	
	(a) A	AX	(b)	CX
	(c) I	OX	(d)	BX
67.	The p	program written in assembly la	ınguag	ge is translated with the help of:
	(a)	Compiler	(b)	Assembler
	(c)	Interpreter	(d)	Decoder
68.	The t	ranslated program into machin	ne cod	e is called:
	(a)	Source program	(b)	Object program
	(c)	System program	(d)	Application software
69.	The c	output of the compiler is called	l:	
	(a)	Program	(b)	Source code
	(c)	Application	(d)	Object code
70.	The s		l to co	nvert a source code into object code are
	(a)	Language translator	(b)	Language processor

	(c)	Both a and b	(d)	Operating system
71.	Wh	ich is not a general purpose regis	ter?	
	(a)	EDX	(b)	EAX
	(c)	EEX	d)	EBX
72.	The	register that holds the current In	struc	tion is called
	(a)	MBR	(b)	MAR
	(c)	IR	(d)	PC
Q.3	WR	THE T FOR TRUE AND F FO	R F.	ALSE STATEMENT:
1.	Bps	stands for byte per second		
2.	In S	implex Transmission mode, comm	ıunic	ation can take place in both directions. (I
3.	Ran	dom access memory is volatile n	nemo	ory. (T
4.	Ope	erating system is an application pr	rogr	am. (I
5.	Exte	ernal buses and internal buses are	sim	ilar. (I
6.	Acc	umulator register is used to conti	ol tł	ne stack in the computer. (I
7.	LIF	O stands for last in first off.		(I
8.	Exp	ansion slot is a place where an ex	kpan	sion card is fitted. (T
9.	Stat	ic RAM holds the data as long as	s pov	ver is supplied to it. (I
10.	The	clock of the computer ticks once	in c	one second just like an ordinary clock.(I
11.	VD	U is an input device.		(I